

ABSTRACT OF THE DISCLOSURE

An interface circuitry of a display chip is disclosed.

According to the present invention, the interface circuitry comprises an input node, a filter and a clamping circuit. The input node is used for receiving an analog image signal. The filter is utilized for processing the analog image signal and providing a processed image signal at an internal node. The clamping circuit is connected between the internal node and a reference level. The clamping circuit is used to clamp the processed image signal by the reference level during a clamping interval.